

PTO/SB/00A (03-03)

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Substitute for PTO Form 01

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 8

Complete if Known

Application Number	10/828,495
Filing Date	April 20, 2004
First Named Inventor	Julian Partridge, et al.
Art Unit	2822
Examiner Name	Thanh Y Tran
Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
T		US-6,919,626 B2	7/19/2005	Burns	
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		US-6,908,792 B2	6/21/2005	Bruce, et al.	
		US-6,884,653	4/26/2005	Larson	
		US-6,803,651	10/12/2004	Chiang	
		US-6,709,893	3/23/2004	Moden, et al.	
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		US-6,683,377	1/27/2004	Shim, et al.	
		US-6,677,670 B2	1/13/2004	Kondo	
		US-6,614,664	9/2/2003	Lee	
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		US-6,576,992	6/10/2003	Cady, et al.	
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		US-6,426,560 B1	7/30/2002	Kawamura, et al.	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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Examiner Signature	Thanh Tran	Date Considered	12/23/05
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known		
				Application Number	10/828,495	
Sheet		2	of	8	Filing Date	April 20, 2004
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		US-5,917,709	6/29/1999	Johnson, et al.	
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		US-5,841,721	11/24/1998	Kwon, et al.	

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T T T		US-5,804,870	9/8/1998	Bums	
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				First Named Inventor	Julian Partridge, et al.
				Art Unit	2822
				Examiner Name	Thanh Y Tran
Sheet	4	of	8	Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763

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TYT		US-5,252,855	10/12/1993	Ogawa, et al.	
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		US-6,452,826	9/17/2002	Kim, et al.	
		US-6,449,159	9/10/2002	Haba	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

Examiner Signature	Thanh Tran	Date Considered	12/23/05
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	8	of	8
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Complete if Known

Application Number	10/828,495
Filing Date	April 20, 2004
First Named Inventor	Julian Partridge, et al.
Art Unit	2822
Examiner Name	Thanh Y Tran
Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763

U.S. PATENT DOCUMENTS					
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		Number-Kind Code ² (if known)			
TNT		US-5,995,370	11/30/1999	Nakamori	
		US-5,959,839	9/28/1999	Gates	
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Examiner Signature	T. Frank Tran	Date Considered	12/23/05
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Sheet	1	of	3
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Application Number	10/828,495
Filing Date	April 20, 2004
First Named Inventor	Julian Partridge, et al.
Art Unit	2822
Examiner Name	Thanh Y Tran
Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763

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		Country Code ² -Number ⁴ -Kind Code ⁵ (if known)				
TJS		WO 03/037053 A1	5/1/2003	Cady, et al.		
		JP 63-153849 (A)	6/27/1998	Matsubara		
		JP 62-230027 (A)	8/10/1987	Nakamura		
		JP 4-209562 (A)	7/30/1992	Uzuka, et al.		✓
		JP 404368167 (A)	12/21/1992	Omura		
		JP 2003/347503 (A)	12/5/2003	Toshiyuki, et al.		✓

**Examiner
Signature**

Thanh Tran

Date
Considered

12/23/05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/828,495
		Filing Date	April 20, 2004
		First Named Inventor	Julian Partridge, et al.
		Art Unit	2822
		Examiner Name	Thanh Y Tran
Sheet 2 of 3	Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763	

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Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		US-			
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		JP 2003/347475 (A)	12/5/2003	Mitsuru		
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		JP 50-29534 (A)	2/5/1993	Nakamura		
		JP 3641047659 (A)	3/8/1986	Soga, et al.		

Examiner Signature	Thanh Tran	Date Considered	12/23/05
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				First Named Inventor	Julian Partridge, et al.
				Art Unit	2822
				Examiner Name	Thanh Y Tran
				Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763
Sheet	3	of	3		

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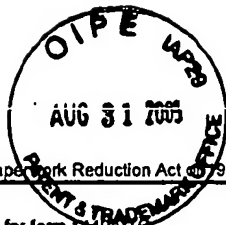
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		Country Code ³ —Number ⁴ —Kind Code ⁶ (if known)				
TYT ↓		JP 359088863 (A)	5/22/1984	Shiragasawa, et al.		
		EP 0426-303 (A2)	10/1/1990	Legg, et al.		
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		Application Number	10/828,495
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Filing Date	April 20, 2004
		First Named Inventor	Julian Partridge, et al.
		Art Unit	2822
		Examiner Name	Thanh Y Tran
		Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763
Sheet	1	of	2

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TYT		Flexible Printed Circuit Technology - A Versatile Interconnection Option. (Website 2 pages) Fjelstad, Joseph. December 3, 2002.	
		Chip Scale Packaging and Redistribution, Paul A. Magill, Glenn A. Rinne, J. Daniel Mis, Wayne C. Machon, Joseph W. Baggs, Unitive Electronics Inc.	
		Dense-Pac Microsystems, 16 Megabit High Speed CMOS SRAM DPS1MX16MKn3	
		Dense-Pac Microsystems, 256 Megabyte CMOS DRAM DP3ED32MS72RW5	
		Dense-Pac Microsystems, Breaking Space Barriers, 3-D Technology 1993	
		Dense-Pac Microsystems, DPS512X16A3, Ceramic 512K X 16 CMOS SRAM Module	
		IBM Preliminary 168 Pin SDRAM Registered DIMM Functional Description & Timing Diagrams	
		3D Interconnection for Ultra-Dense Multichip Modules, Christian VAL, Thomson-CSF DCS Computer Division, Thierry Lemoine, Thomson-CSF RCM Radar Countermeasures Division	
		High Density Memory Packaging Technology High Speed Imaging Applications, Dean Frew, Texas Instruments Incorporated	
		Vertically-Integrated Package, Alvin Weinberg, Pacesetter, Inc. and W. Kinzy Jones, Florida International University	

Examiner Signature	Thanh Tran	Date Considered	12/23/05
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TYT		1992 Proceedings, 42nd Electronic Components & Technology Conference, May 18-20, 1992	
		Research Disclosure, Organic Card Device Carrier, 31318, May 1990, No. 313	
		IBM Technical Disclosure Bulletin, Vol. 23, No. 12, May 1981	
		IBM Technical Disclosure Bulletin, Vol. 20, No. 11A, Apr. 1978	
		IBM Technical Disclosure Bulletin, vol. 32, No. 3B, Aug. 1989	
		Orthogonal Chip Mount - A 3D Hybrid Wafer Scale Integration Technology, International Electron Device Meeting, IEDM Technical Digest, Washington, D.C., Dec. 6-9, 1987	

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